

DETAILED ACTION

Status of the Application

1. This Office Action addresses the amendment filed in response to the Non-Final Rejection mailed December 8, 2009. Claims 12-32 are pending in the application. The applicants have added claim 32. The applicants have not amended any claims. The applicants previously cancelled claims 1-11.

Allowable Subject Matter

2. Claims 12-32 are allowed.

The following is an examiner's statement of reasons for allowance: The prior art discloses, teaches, and suggests:

For claim 12:

a electronic component operable with an AC voltage, the electric component comprising:

at least one input; at least one output; and

a pair of functionally identical electronic sub-components;

wherein the at least one input of the electronic component is connected to a respective input of the two functionally identical electronic sub-components;

wherein the at least one output of the electronic component is connected to a respective output of the two functionally identical electronic sub-components; and

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wherein the electronic component is configured such that at the at least one output only one output signal of a first sub-component of the pair of functionally identical electronic sub-components can be picked up during a first half-wave of an AC voltage, whereas only one output signal of the second sub-component of the pair of functionally identical electronic sub-components can be picked up during a second half-wave of the AC voltage.

The prior art does not disclose, teach, or suggests such an electronic component, wherein the functionally identical electronic sub-components are connected in parallel.

For claim 22:

an electronic arrangement comprising:

a read device;

an ID tag with an electric component comprising:

a first sub-component with an input and an output;

a second sub-component with an input and an output;

an AC signal received by the inputs of the first and second sub-components, the AC signal having a first half-wave and a second half-wave;

means for providing an output from only the first sub-component during the first half-wave; and

means for providing an output from only the second sub-component

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during the second half-wave;

wherein the ID tag and read device are configured to communicate with each other without contact.

The prior art does not disclose, teach, or suggests such an electronic component, wherein the first and the second sub-component are connected in parallel.

For Claim 32

The following is an examiner's statement of reasons for allowance: The prior art discloses, teaches, and suggests:

an electronic component operable with an AC voltage, the electric component comprising:

at least one input;

at least one output; and

a pair of functionally identical electronic sub-components;

wherein the at least one input of the electronic component is connected to a respective input of the two functionally identical electronic sub-components;

wherein the at least one output of the electronic component is connected to a respective output of the two functionally identical electronic sub-components; and

wherein the electronic component is configured such that at the at least one output only one output signal of a first sub-component of the pair of

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functionally identical electronic sub-components can be picked up during a first half-wave of an AC voltage, whereas only one output signal of the second sub-component of the pair of functionally identical electronic sub-components can be picked up during a second half-wave of the AC voltage.

The prior art does not disclose, teach, or suggests such an electronic component, wherein the functionally identical electronic sub-components are identically oriented and connected in parallel.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to JOHN F. MORTELL whose telephone number is (571)270-1873. The examiner can normally be reached on IFP.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Daniel J. Wu can be reached on (571)272-2964. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications

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may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/JM/

/Daniel Wu/
Supervisory Patent Examiner, Art Unit 2612